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SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)
MCA III Year I Semester Regular Examinations Febuary-2022
DESIGN PATTERNS

Time: 3 hours

Max. Marks: 60

(Answer all Five Units 5 x 12 = 60 Marks)

UNIT-I

1 List the different sections of design patterns. Explain about them. L1 12M

OR

2 Explain in detail about use and organizing the Design pattern. L1 12M

UNIT-II

3 a Explain the supporting Multiple Window Systems. L2 6M

b Explain the Embellishing the user interface. L2 6M

OR

4 Consider any application of Case study? How to support the Document editor design patterns? L1,L2 12M

UNIT-III

5 a List and explain the implementation issues of factory method. L2 6M

b Give brief description about the singleton creational pattern. L1 6M

OR

6 Explain in details about the Consequence & Collaboration of Factory Method with demonstration an example. L1,L2 12M

UNIT-IV

7 a What is the motivation and design a structure of application using adapter pattern? Explain it in detail. L4 6M

b Explain the applicability and structure of a bridge design pattern. L2 6M

OR

8 a Draw and explain the structure and object diagram for flyweight pattern. L2 6M

b Describe briefly about the implementation issues of a proxy pattern. L2 6M

UNIT-V

9 a What is an Iterator design pattern? Consider any application to implement design architecture of Iterator design pattern. L2,L3 6M

b Explain behavioral patterns for chain of responsibility pattern. L2 6M

OR

10 Explain in detail memento design pattern. L2 12M

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